

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Patent Application**

Applicant(s): M. Kapur et al.  
Docket No.: YOR920030306US1  
Serial No.: 10/650,222  
Filing Date: August 28, 2003  
Group: 2117  
Examiner: Cynthia H. Britt  
  
Title: Self-Synchronizing Pseudorandom  
Bit Sequence Checker

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**AMENDMENT AFTER ALLOWANCE UNDER 37 C.F.R. §1.312**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants respectfully request amendment of the application as follows prior to issuance: